

4/15/04

PTO-1449 Information Disclosure Citation in an Application		Application No. Docket Number 064747.1017		Applicant(s) James D. Ballew et al. Shannon et al. Group Art Unit 2191 Filing Date April 15, 2004			
U.S. PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
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FOREIGN PATENT DOCUMENTS							
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NON-PATENT DOCUMENTS							
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					DATE
TV	O	Patent Application entitled, "High Performance Computing System and Method", 47 pages specification, claims and abstract, 10 pages of drawings, inventors Ballew et al, Attorney Docket No. 064747.1012					04/15/2004
TV	P	Patent Application entitled, "System and Method for Cluster Management Based on HPC Architecture", 49 pages specification, claims and abstract, 10 pages of drawings, inventors Ballew et al, Attorney Docket No. 064747.1014					04/15/2004
TV	Q	Patent Application entitled, "System and Method for Detecting and Managing HPC Node Failure", 49 pages specification, claims and abstract, 10 pages of drawings, inventors Ballew et al, Attorney Docket No. 064747.1015					04/15/2004
TV	R	Patent Application entitled, "Graphical User Interface for Managing HPC Clusters", 48 pages specification, claims and abstract, 10 pages of drawings, inventors Ballew et al, Attorney Docket No. 064747.1016					04/15/2004
	S						
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EXAMINER TED T. VO				DATE CONSIDERED 4/27/07			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.							

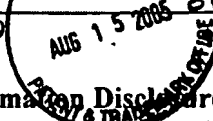
8/1/05

PTO-1449		Application No. 10/825,021		Applicant(s) Shannon V. Davidson et al.		
Information Disclosure Citation in an Application		Docket Number 064747.1017		Group Art Unit 2191	Filing Date April 15, 2004	
U.S. PATENT DOCUMENTS						
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FOREIGN PATENT DOCUMENTS						
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NON-PATENT DOCUMENTS						
	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)				DATE	
FTV	E	Communication from the European Patent Office, European Search Report for Application No. EP 05 25 2235, 3 pages, 07/22/2005, 3 pages				07/22/2005
FTV	F	Rzymianowicz et al., "Clustering SMP Nodes with the ATOLL Network: A Look into the Future of System Area Networks," Proceedings of High Performance Computing, 8th International Conference, May 8, 2000, 10 pages				05/08/2000
FTV	G	Bhanot et al., "The BlueGene/L Supercomputer," 20th International Symposium on Lattice Field Theory," vol. 119, June 2002, 8 pages				06/2002
FTV	H	Advanced Micro Devices et al., "Hypertransport Technology I/O Link - A High-Bandwidth I/O Architecture," July 20, 2001, pp. 1-25				07/20/2001
FTV	I	Pinkston et al., "InfiniBand: The "De Facto" Future Standard for System and Local Area Networks or Just a Scalable Replacement for PCI Buses?," Cluster Computing-Kluwer Academic Publishers, vol. 6, no. 2, 2003				2003
FTV	J	Culler et al., "Parallel Computer Architecture - A Hardware/Software Approach, Interconnection Network Design," Morgan Kaufmann, 1999, 30 pages				1999
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PTO-1449 Information Disclosure Citation In an Application		Application No. 10/825,021		Applicant(s) Shannon V. Davidson et al.	
Docket Number 064747.1017		Group Art Unit		Filing Date April 15, 2004	

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FOREIGN PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
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NON-PATENT DOCUMENTS			
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
TTL	E	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012031, 6 pages, 08/01/2005 and Written Opinion of the International Search Authority, 5 pages	08/01/2005
TTL	F	Chang et al., "Performance Improvement of Allocation Schemes for Mesh-Connected Computers," Journal of Parallel and Distributed Computing, Academic Press, Duluth, MN, vol. 52, no. 1, July 10, 1998, 30 pages	07/10/1998
TTL	G	Wanqian, et al. "Non-Contiguous Processor Allocation Algorithms for Distributed Memory Multicomputers," Supercomputing '94, Proceedings, Washington, D.C., November 14-18, 1994, 10 pages	11/14/1994 - 11/18/1994
TTL	H	Krevat et al., "Job Scheduling for the BlueGene/L System," Lecture Notes in Computer Science, vol. 2537, July 24, 2002, 18 pages	07/24/2002
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U.S. PATENT AND TRADEMARK OFFICE


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10/10/05

PTO-1449		Application No. 10/825,021		Applicant(s) Shannon V. Davidson et al.			
Information Disclosure Citation In an Application <div style="border: 1px solid black; border-radius: 50%; width: 100px; height: 100px; display: flex; align-items: center; justify-content: center; margin: 10px auto;"> <div style="writing-mode: vertical-rl; transform: rotate(180deg); font-size: 8px;">PATENT & TRADEMARK OFFICE</div> <div style="text-align: center;"> OCT 10 2005 </div> </div>		Docket Number 064747.1017		Group Art Unit	Filing Date April 15, 2004		
		U.S. PATENT DOCUMENTS					
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
ITV	A	4,868,818	09/19/1989	Madan	371	11.3	10/29/1987
ITV	B	5,020,059	05/28/1991	Gorin et al.	371	11.3	03/31/1989
ITV	C	5,748,882	05/05/1998	Huang	395	184.01	05/08/1996
ITV	D	5,926,619	07/20/1999	Badovinatz et al.	395	182.02	01/05/1998
ITV	E	6,453,426	09/17/2002	Gamache et al.	714	4	03/26/1999
ITV	F	6,629,266	09/30/2003	Harper et al.	714	38	11/17/1999
ITV	G	2003/0005276	01/02/2003	French et al.	713	2	06/28/2001
ITV	H	2003/0097487	05/22/2003	Rietze et al.	709	325	11/20/2001
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
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ITV	I	WO 03/005192	01/16/2003	PCT	G06F	9/445	X
ITV	J	EP 0 981 089 A2	02/23/2000	European Patent Office	G06F	11/14	X
NON-PATENT DOCUMENTS							
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					DATE
ITV	K	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012313, 7 pages, 09/20/2005, and Written Opinion of the International Search Authority, 7 pages					09/20/2005
ITV	L	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012316, 7 pages, 09/14/2005, and Written Opinion of the International Search Authority, 7 pages					09/14/2005
ITV	M	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012242, 7 pages, 09/19/2005, and Written Opinion of the International Search Authority, 9 pages					09/19/2005
ITV	N	Davidson, U.S. Patent Application Serial Number 10/991,754, filed 11/17/2004, entitled "Fault Tolerance and Recovery in a High-Performance Computing (HPC) System, 117 pages					11/17/2004
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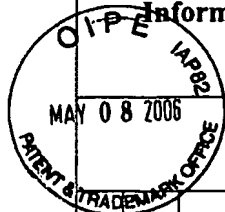
PTO-1449		Application No. 10/825,021		Applicant(s) Shannon V. Davidson et al.			
		Docket Number 064747.1017		Group Art Unit 291	Filing Date April 15, 2004		
		U.S. PATENT DOCUMENTS					
	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
FTV	A	5,301,104	04/05/1994	Yalamanchili	395	800	08/07/1990
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NON-PATENT DOCUMENTS							
	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					DATE	
FTV	E	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012314, 6 pages, 09/20/2005, and Written Opinion of the International Search Authority, 7 pages					Mailed 02/10/2006
FTV	F	Feitelson, "Job Scheduling in Multiprogrammed Parallel Systems", IBM Research Report, August 1997, pages 1-4, XP002942107					August 1997
FTV	G	Kandlur et al., "Hypercube Management in the Presence of Node Failures", third conference on hypercube concurrent computers and applications ACM New York, 1988, pages 328-336, vol. 1, XP002364194					1998
FTV	H	Wenjian et al., "Efficient Processor Allocation for 3D Tori", Parallel Processing Symposium 1995, Proceedings, 9th International, April 25-28, 1995, IEEE Comput. Soc., pages 466-471, XP010135921					1995
FTV	I	Ma et al., "E-Kernel: An Embedding Kernel on the IBM Victor V256, Multiprocessor for Program Mapping and Network Reconfiguration, IEEE Transactions on Parallel and Distributed Systems, IEEE Service Center, vol. 5, no. 9, September 1, 1994, pages 977-994, XP000460490					September 1, 1994
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5/8/06

PTO-1449		Application No. 10/825,021		Applicant(s) Shannon V. Davidson et al.	
Information Disclosure Citation In an Application		Docket Number 064747.1017		Group Art Unit	
				Filing Date April 15, 2004	



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		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
TL	A	2003/0154112	08/14/2003	Neiman et al.	705	5	10/29/2002
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NON-PATENT DOCUMENTS

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
TL	E	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012643, 6 pages, 04/19/2006, and Written Opinion of the International Search Authority, 5 pages	Mailed 04/19/2006
TL	F	Haynes et al., "A Visualization Tool for Analyzing Cluster Performance Data," 42nd Annual Symposium on Foundations of Computer Science, (FOCS 2001), October 8, 2001, pages 295-302	October 14-17 2001
TL	G	Patel et al., "Sage: An Application Development Tool Suite for High Performance Computing Systems," Aerospace Conference Proceedings, 2000, IEEE, Vol 11, pages 491-500	March 18-25 2000
TL	H	Baraglia et al, "RsdEditor: A Graphical User Interface for Specifying Metacomputer Components," Heterogeneous Computing Workshop Proceedings, 9th Cancun, Mexico, pages 336-345	May 1, 2000
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U.S. PATENT AND TRADEMARK OFFICE

DAL01:896458.1

6/14/06

PTO-1449		Application No. 10/825,021		Applicant(s) Shannon V. Davidson et al.			
Information Disclosure Citation In an Application		Docket Number 064747.1017	Group Art Unit 2191	Filing Date April 15, 2004			
U.S. PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
TV	A	5,682,491	10/28/1997	Pechanek et al.			
TV	B	6,735,660	05/11/2004	Osten et al.			
TV	C	2003/0188071	10/02/2003	Kunjan et al.			
TV	D	2004/0024949	02/05/2004	Winkler et al.			
TV	E	2004/0268000	12/30/2004	Barker et al.			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
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NON-PATENT DOCUMENTS							
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					DATE
TV	H	Wong, William, "Switch-Chip Fuels Third-Generation InfiniBand," November 10, 2003, Electronic Design, 2 pages					11/16/2003
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DAL01:896458.1

10/13/06

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Docket Number 064747.1017		Group Art Unit 2191		Filing Date April 15, 2004	

U.S. PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
TV	A	5,280,607	01-18-1994	Bruck et al.			
TV	B	5,805,785	09-08-1998	Dias et al.			
TV	C	6,088,330	07-11-2000	Bruck et al.			
TV	D	6,460,149	10-01-2002	Rowlands et al.			
TV	E	6,477,663	11-05-2002	Laranjeira et al.			
TV	F	6,496,941	12-17-2002	Segal et al.			
TV	G	6,718,486	04-06-2004	Rosellin et al.			
TV	H	6,820,221	11-16-2004	Fleming			
TV	I	6,918,051	07-12-2005	Block et al.			
TV	J	6,918,063	07-12-2005	Ho et al.			
TV	K	6,952,766	10-04-2005	Dervin et al.			
TV	L	7,016,299	04-21-2006	Kashyan			
TV	M	7,028,228	04-11-2006	Lovy et al.			
TV	N	7,093,004	08-15-2006	Bernardin et al.			
TV	O	2003/0009551	01-09-2003	Benfield et al.			

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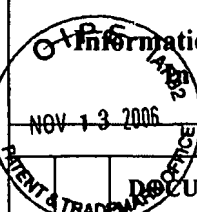
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11/13/06

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TR	A	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012500 and Written Opinion of the International Search Authority, mailed 08/01/2005, 12 pages				08/01/2005
TR	B	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012489 and Written Opinion of the International Search Authority, mailed 11/18/2005, 14 pages				11/18/2005
*	C	Unknown, "HP AlphaServer SG User Guide," Internet Article, Online!, July 12, 2004, retrieved from the internet: URL: http://web1.quadrics.com/online/docs/AlphaServer/Eagle/html/AlphaServerUserGuide/ retrieved on 07/19/2005				07/12/2004
*	D	Choo et al., "An Efficient Submesh Allocation Scheme for 3D Torus Multicolor Systems," Parallel Algorithms/Architecture Synthesis, 1997, Proceedings, Second Aizu International Symposium Aizu-Wakamatsu, Japan, March 17-21, 1997				03/17/1997 03/21/1997
TR	E	Unknown, "Message Passing Interface (MPI)," Internet Article, Online!, December 23, 2003, pages 1-33, retrieved from the internet: URL: http://web.archive.org/web/20040102194825/http://www.llnl.gov/computing/tutorial/s/mpi/ , retrieved on 07/18/2005, pages				12/23/2003
TR	F	Panagiotis, Christias et al., "inetd - Internet Services Daemon," pages 1-4, 1994 Man-cgi 1.15S, 1995 Modified for Solaris 2.3				1994
*	G	Unknown, "Cisco Local Director Configuration and Command Reference Guide," Internet Article, Online, April 4, 2001				04/04/2001
*	H	Moore et al., "Managing Mixed-Use Clusters with Clusters-on-Demand," Internet Article, November 2002				November 2002
TR	I	Di Santo et al., "Kernel Implementation of Locality-Aware Dispatching Techniques for Web Server Clusters," Cluster Computing, 2003 Proceedings, Pages 154-162				12/01/2003
TR	J	Ross et al., "3.3 Connectionless Transport," February 22, 2001, pages 1-4, retrieved on 11/01/2005				11/02/2005
TR	K	Davidson et al., Patent Application Serial No. 10/825,345, entitled, "System and Method for Computer Cluster Virtualization Using Dynamic Boot Images and Virtual Disk, 47 pages of specification, 10 pages of drawings, Attorney Docket No. 064747.1011				04/15/2004
TR	L	Davidson, Patent Application Serial No. 10/991,598, entitled, "Scheduling in a High Performance Computing (HPC) System", 111 pages specification, claims and abstract, 11 pages of drawings, Attorney Docket No. 064747.1048				11/17/2004
TR	M	Davidson, Patent Application Serial No. 10/991,994, entitled, "On-Demand Instantiation in a High-Performance Computing (HPC) System", 110 pages specification, claims and abstract, 12 pages of drawings, Attorney Docket No. 064747.1049				11/17/2004
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* See action 1.98(b)

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NON-PATENT DOCUMENTS							
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					DATE
TW	A	The Intellectual Property Bureau Ministry of Economic Affairs, Office Action for Application No. 94111492, received November 27, 2006, 2 pages					Received- 4/27/2006
TW	B	Jiun-Sheng et al., "Network Topology Structure on Board of Computer Cluster," Publication No. TW532011B, Data Supplied from the esp@cenet database - Worldwide, 1 page					Received 12/18/2006
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		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					DATE
F	A	Hans-Ulrich Heiss, "Processor Management in Two-Dimensional Grid-Architectures," Interner Bericht Nr. 20/92, XP002416087, 51 pages					December 1992
F	B	EPO Registered Letter, Application No. 05 252 239.8-1243, 6 pages					Mailed 02/02/2007
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TR	A	6,167,502	12/26/2000	Pechanek et al.			
TR	B	2001/0049740	12/01/2001	Karpoff			
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